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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: : Before the Examiner:
Balaram Sinharoy : William H. Wood

Serial No.: 09/435,070 : Group Art Unit: 2124

Filed: November 4, 1999 :

Title: CIRCUITS, SYSTEMS AND : IBM Corporation
METHODS FOR PERFORMING : Intellectual Property Law
BRANCH PREDICTIONS BY : 11400 Burnet Road
SELECTIVELY ACCESSING BIMODAL : Austin, Texas 78758
AND FETCH-BASED HISTORY TABLES :

REPLY UNDER 37 C.F.R. § 1.116**RECEIVED****AUG 05 2004****Technology Center 2100**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

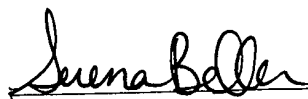
Dear Sir:

In response to the Office Action having a mailing date of May 17, 2004 (Paper No. 15), having a three-month shortened statutory period for response set to expire on August 17, 2004, please reconsider the rejections of the claims in view of the following remarks:

Remarks begin on page 2 of this paper.

CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence (along with any item referred to as being enclosed herewith) is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 28, 2004.


SignatureSerena Beller

(Printed name of person certifying)